

- Pending
- Active
- Failed
- Saved
- S1: (5901) (clock or pulse) near9 width near9 limit\$4
- S2: (54938) (clock or pulse) near9 design\$4
- S3: (1380935) (override or select\$4 or detect\$4)near9 signal
- S4: (681) S1 and S2 and S3
- S5: (63348) (inverter or gate)near9 delay
- S6: (221) S4 and S5
- S7: (467567) (clock or pulse) nsar9 generate\$4
- S8: (212) S6 and S7
- S9: (36) (phase-locked-loop) or PLL)and S8
- S10: (3) pulse train (near9 width near9 limit\$4) and S9
- Favorites
- Tagged (0)

6704908

```
Source: [616881b.wsp:1] 6704908
File: [616881b.wsp:1] 6704908
Search: [616881b.wsp:1] 6704908
Override: [616881b.wsp:1] 6704908
pulse same limit$4 same multiplex$4 same
override$3
```

U	I	Document ID	Title	Comments (Current)
<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6114833 A	Monitoring and controlling system for battery and battery charger	329/10 320/10
<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6087305 A	Monitoring and controlling system for battery and battery charger	329/10
<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5535337 A	Port circuit for a token ring concentrator having phase lock loop timing recovery with a	709/24 713/50